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		Docket: 1011-58140	App: 09/872,212
		Applicant: Kebichi et al.	
		Filed: May 31, 2001	Art Unit: 2818 2133
<b>OTHER DOCUMENTS</b>			
Q		✓	MBISTArchitect Reference Manual, V8.8_1.100, September 2000, Chapter 1 - Introduction
Q		✓	MBIST Architect – Anatomy of the BIST Controller, published by Mentor Graphics Corporation, April 1999
Q		✓	MBISTArchitect Reference Manual, V8.9_1.10, Defining the Model – DFT Library Modeling for Memories, December 2000
Q		✓	MBIST Architect – UDA Kernel Design, published by Mentor Graphics, April 1999
Q		✓	Burgess, “Test and Diagnosis of Embedded Memory Using BIST,” <i>EE-Evaluation Engineering</i> , March 2000.
Q			Mentor Graphics, “Built-In Self-Test Process Guide, Software Version 8.9_1.10,” December 2000.
EXAMINER: <i>Guy Ramane</i>		DATE <i>2/28/04</i>	
*Examiner: Initial if considered, whether or not in conformance with MPEP 609; draw line through cite if not in conformance and not considered. Send copy.			

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